Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claims 1-9 (Cancelled)

10. (Currently Amended) [[The]] A mobile electronic apparatus ~~according to Claim 15,~~ comprising:

a display control device, a data processing unit, and a dot matrix liquid crystal display device, said display control device having a display memory which is capable of storing display data for the display device and into which display data are written in a prescribed number of bits at a time, the display control device successively reading the display data out of the display memory and forming and supplying a drive signal to the display device,

wherein said display memory includes:

a memory array provided with a plurality of memory cells arranged in a matrix form,

a plurality of word lines to which selection terminals for the memory cells are connected,

a plurality of bit lines which are arranged in a direction to cross the word lines and to which data input/output nodes for the memory cells are connected,

input transfer means and output transfer means being connected to said bit lines, data transferring by said input transfer means resulting in writing of data into the memory cells connected to a word line in a selected state, and data transferring by said output transfer means resulting in reading of data out of the memory cells connected to the word line in a selected state, and

a plurality of first data latch means capable of successively taking in the display data in said prescribed number of bits at a time, and display data held by the first data latch means can be collectively transferred by said input transfer means to the bit lines of said display memory in a number of bits at a time equal to an integral multiple of (n times) the number of bits of the display data taken into the first data latch means,

wherein the data processing unit generates display data to be written into said display memory and sets information on their writing position,

wherein the display device carries out displaying with a display drive signal read out of said display memory and formed by said display control device based on the display data, and

wherein said display control device further comprises a segment drive means for generating signals for driving segment electrodes of said ~~liquid crystal~~ display device, and a common electrode drive circuit for generating a signal for driving common electrodes of said ~~liquid crystal~~ display device is configured as a semiconductor integrated circuit over a separate semiconductor chip from [[the]] a semiconductor chip over which said display control device is formed, and the common electrode drive circuit is configured of an element higher in withstand voltage than the elements constituting said display control device.

11. (Cancelled)

12. (Currently Amended) [[The]] A display control device ~~according to Claim 11, further~~ formed over a single semiconductor substrate, comprising:

a memory for storing display data to be displayed on a liquid crystal panel;

a k-bit first external terminal to which display data to be stored in said memory are supplied from a microprocessor;

a plurality of second external terminals for outputting drive signals for driving said liquid crystal panel on the basis of m-bit read data from said memory;

a first latch circuit connected between the input of said memory and said first external terminal and capable of storing m-bit display data;

a transfer circuit for selecting, for each integral multiple (multiple by n) of said k bits, display data of not more than said m bits (k·n) in said first latch circuit and transferring them to bit lines of said memory; and

a second latch circuit provided between said transfer circuit and said first latch circuit and capable of storing said m-bit display data,

said second latch circuit outputting display data of said ~~number of~~ m bits (k·n) to said transfer circuit.

Claims 13-24 (Cancelled)